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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	10/614533
		Filing Date	Conservatory Hearing 7/7/2003
		First Named Inventor	Edouard D. de Fresan
		Group Art Unit	2823
		Examiner Name	Dang
Sheet	of	Attorney Docket Number	SC113422P C01

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
T.D	AA	4,300,150	11/10/1981	Colak	
	AB	5,523,601	06/04/1998	Yanagigawa	
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	AE	5,801,431	09/01/1998	Ranjan	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
T.D	AI	EP 0 295 391 A1	04/21/1988	Eklund	
T.D	AJ	EP 0 741 416 A1	11/06/1996	Depetro et al.	
T.D	AK	DE 195 35 140 A1	03/28/1996	Kitamura et al.	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
T.D	AL	Bergemont et al., "High Voltage Driver Built in a Low Voltage 0.18 μ m CMOS for Cache Redundancy Applications in Microprocessors," ISPSD, May 22-25, 2000, Toulouse, France.	
T.D	AM	Kim et al., "A 650V rated RESURF-type LDMOS Employing an Internal Clamping Diode for Bulk Breakdown without EPI Layer," Proceedings of 2001 International Symposium on Power Devices and ICs, Osaka, Japan, 4-7 June 2001, p. 347-350.	
T.D	AN	PCT/US02/18353 PCT Search Report mailed 15 April 2003.	

Examiner Signature	Date Considered
<i>E. Dang</i>	8/17/04

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